

## **Step Motor Device Driver**

## **Step Motor**

#### Step Motor

- ✓ Step Motor
- ✓ source 가
- $\checkmark$



# Step Motor(2)





#### **Step Motor**









#### **STEP MOTOR**

#### Step Motor







#### **PXA255-Pro Step Motor**





# Step Motor Databit

■ 8bit 가 ■ PXA255 FPGA 1

| Bit           | 7     | 6 | 5  | 4       | 3  | 2 | 1   | 0       |
|---------------|-------|---|----|---------|----|---|-----|---------|
| Step<br>Motor | nB    | В | nA | step2 A | nB | В | nA  | step1 A |
|               |       |   |    |         |    |   |     |         |
|               | 2 bit |   |    |         |    | 1 | bit |         |



# Half Step Operation ✓ 1 -2 -1 -2 ✓ 1/2 ✓ 1/2

| Saguanaa |      | Output |      |      |        |  |
|----------|------|--------|------|------|--------|--|
| Sequence | А    | nA     | В    | nB   | Output |  |
| 1        | 1(H) | 0(L)   | 0(L) | 0(L) | А      |  |
| 2        | 0    | 0      | 1    | 0    | В      |  |
| 3        | 0    | 1      | 0    | 0    | nA     |  |
| 4        | 0    | 0      | 0    | 1    | nB     |  |

,



# (2)

| Full Step    | o Operation |       |          |              |      |              |      |
|--------------|-------------|-------|----------|--------------|------|--------------|------|
| $\checkmark$ | 4 2         |       |          | 가            |      |              |      |
| $\checkmark$ | (Torque)    |       |          |              |      |              |      |
| ■<br>1       | Cm 1m       |       | 1Cm<br>( | 1m<br>: gfcm | n Kg | ( )<br>fcm ) | (1 ) |
|              | Saguanaa    | Input |          |              |      | Quitout      |      |
|              | Sequence    | A     | nA       | В            | nB   | Oulpul       |      |
|              | 1           | 1(H)  | 0(L)     | 1(H)         | 0(L) | AB           |      |
|              | 2           | 0     | 1        | 1            | 0    | nAB          |      |
|              | 3           | 0     | 1        | 0            | 1    | nAnB         |      |
|              | 4           | 1     | 0        | 0            | 1    | AnB          |      |



## **Device driver source code**

#include <linux/module.h>
#include <asm/hardware.h>
#include <asm/uaccess.h>
#include <linux/kernel.h>
#include <linux/kernel.h>
#include <linux/fs.h>
#include <linux/fs.h>
#include <linux/types.h>
#include <linux/types.h>
#include <linux/ioport.h>
#include <linux/ioport.h>
#include <linux/delay.h>
#include <linux/delay.h>
#include <linux/init.h>
#include <linux/version.h>
#include <linux/version.h>
#include <linux/version.h>

ex)MOD\_INC\_USR\_COUNT

//copy\_from\_user()

//udelay()
 //GPIO controller
 //init\_module() cleanup\_module()

#define IOM\_STEP\_MAJOR 247 //define major number #define IOM\_STEP\_NAME "STEP" //define device name #define IOM\_STEP\_ADDRESS 0xC0000C

//

# **Device driver source code (2)**

| (unsigned short)(0x1) |
|-----------------------|
| (unsigned short)(0x5) |
| (unsigned short)(0x4) |
| (unsigned short)(0x6) |
| (unsigned short)(0x2) |
| (unsigned short)(0xa) |
| (unsigned short)(0x8) |
| (unsigned short)(0x9) |
|                       |

#define iom\_step\_init init\_module

```
int iom_step_open(struct inode *, struct file *);
int iom_step_release(struct inode *, struct file *);
ssize_t iom_step_write(struct file *, const char *, size_t, loff_t *);
int __init iom_step_init(void);
void cleanup_module(void);
```



# **Device driver source code (3)**

```
//Global variable
static int step_usage = 0;
static int step_major = 0;
static char mode;
static int check = 0;
```

static unsigned short \*iom\_step\_addr;



# **Device driver source code (4)**

```
into iom_step_open(struct inode *minode, struct file *mfile)
ſ
    if(step_usage != 0) return -EBUSY;
    MOD_INC_USE_COUNT;
    step_usage = 1;
    check = 0:
    return 0;
}
int iom_step_release(struct inode *minode, struct file *mfile)
    MOD_DEC_USE_COUNT;
    step_usage = 0;
    outw(0,iom_step_addr);
    return 0;
}
```



# **Device driver source code (5)**

```
size iom_step_write(struct file *inode, const char *gdata, size_t length,
loff t *off what)
í
    if(check == 0)
    {//
                            check
          const char *temp = gdata;
          copy_from_user(&mode, temp, 1);
          check = 1:
     else if(check ==1)
    {//
                            Step Motor
          const char *tmp = gdata;
          unsigned short speed;
          copy_from_user(&speed, tmp, 2);
#if O
          //Full Step Operation
          outw(AB,iom_step_addr);
          udelay(speed);
```



# **Device driver source code (6)**

```
outw(_AB,iom_step_addr);
udelay(speed);
outw(_A_B,iom_step_addr);
udelay(speed);
outw(A_B,iom_step_addr);
udelay(speed);
```

#else

```
//Half Step Operation
if(mode == 'a'){//
    outw(A,iom_step_addr);
    udelay(speed);
    outw(B,iom_step_addr);
    udelay(speed);
    outw(_A,iom_step_addr);
    udelay(speed);
    outw(_B,iom_step_addr);
    udelay(speed);
    udelay(sp
```



## **Device driver source code (7)**

```
else if(mode == 'b'){//
                 outw(_B,iom_step_addr);
                 udelay(speed);
                 outw(_A,iom_step_addr);
                 udelay(speed);
                 outw(B,iom_step_addr);
                 udelay(speed);
                 outw(A,iom_step_addr);
                 udelay(speed);
        }
#endif
    return length;
```



}

# **Device driver source code (8)**

```
into init iom step init(void)
    int result:
    result = register_chrdev(IOM_STEP_MAJOR,IOM_STEP_NAME,&iom_step_fops);
    if(result < 0) {
         printk(KERN_WARNING"Can't get any major\n");
         return result:
    step major = IOM STEP MAJOR;
    iom step addr = ioremap(IOM STEP ADDRESS,0x02);
    printk("init module, %s major number : %d\n",IOM STEP NAME,step major);
    return 0;
void cleanup module(void)
    iounmap(iom_step_addr);
    if(unregister chrdev(step major, IOM STEP NAME))
         printk(KERN_WARNING"%s DRIVER CLEANUP FALLED\n",IOM_STEP_NAME);
```



## **Application source code**

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <sys/types.h>
#include <sys/stat.h>
#include <fcntl.h>
#include <sys/ioctl.h>
#include <signal.h>
static char mode;
into main(void)
     int fd;
     unsigned short c = 0xffff;
     printf("a. spin\n");//menu
     printf("b. back spin\n");
     printf("select->");
     scanf("%c", &mode);
     printf("\n");
```

# **Application source code (2)**

```
while(1){
    fd = open("/dev/STEP",O_WRONLY);//divice open
    if (fd < 0){
        printf("Device Open Error\n");
        exit(1);
    write(fd, &mode, 1);//write mode
    for(;;){
        if(c < 0x1000){
               c = 0x1000;//
                                     가
        else c -=0x100;//increase speed
        write(fd,&c,2);//write speed
    close(fd);
```



#### Makefile

INCLUDEDIR := /home/max233/linux-2.4.19-cd/include

```
CFLAGS := -D__KERNEL__ -I$(INCLUDEDIR) -Wall -O2 -DMODULE
```

```
CROSS_COMPILE := arm-linux-
```

```
CC=$(CROSS_COMPILE)gcc
LD=$(CROSS_COMPILE)Id
```

```
all: step_driver test_step

step_driver:

$(CC) $(CFLAGS) -c step_driver.c -o step_driver.o

test_step:

$(CC) -I$(INCLUDEDIR) -o test_step test_step.c

clean:

rm -f *.o
```

```
rm -f test_step
```



#### Step Motor Device Driver

- ✓ device driver file interface (node
- ✓ Device driver major number
- int register\_chrdev(unsigned int major, const char \*name, struct file\_operations \*fops)
  - Major : major number. 0
  - Name : device
  - Fops : device file
- int unregister\_chrdev(unsigned int major, const char \*name)



(2)



#### (3)

#### Major number minor number

- $\checkmark$
- ✓ Major number :
- ✓ Minor number :

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- ✓ major number 가
   ✓ register\_chrdev() major number
   ✓ major number가
- ✓ Major minor
   inode i\_rdev 16bit
  - 8bit major,

8bit minor .



#### mknod

- ✓ mknod [device file name] [type] [major] [minor]
  - Ex] %mknod /dev/STEP c 247 0
- mdev\_t : major, minor number
  - ✓ MAJOR() : kdev\_t major number
    - Ex] MAJOR(inode->i\_rdev);
  - ✓ MINOR() : kdev\_t minor number
  - ✓ cat /proc/devices



## (5)

| insmod   | module           | (install)      |                 |                 |
|----------|------------------|----------------|-----------------|-----------------|
| rmmod    | r                | odules (unl    | oad)            |                 |
| lsmod    | Load mo          | odule          |                 |                 |
| depmod   | Module           | symbol         | Makefile        | dependency file |
| modprobe | depmod<br>module | depe<br>module | endency<br>load |                 |

#### (6)

#### Device Driver



|   |               | makefile Makefile   | step_driver.c, test application                        | test_step.c, |
|---|---------------|---|--|--------------|
|   | make<br>% mak | e   | 2 files  |              |
| 1 |               | step_driver.o tes<br>minicom nfs  | t_step target .<br>nfs                                 |              |
|   |               | ▼ <u>root@localhost:~/test2</u><br>파일( <u>F</u> ) 편집( <u>E</u> ) 보기( <u>V</u> ) 터<br># is<br>step_driver.o <b>test_step</b><br># [] | ■ ■ ■<br>미널( <u>T</u> ) 가기( <u>G</u> ) 도움말( <u>H</u> ) |              |

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## (8)

| 💙 root@la   | oalhost:  | /test2                                  |                 |                |                 | (())(()((), 🖕 🖛 🗖 |   |
|---|---|---|-----------------|----------------|-----------------|-------------------|---|
| 파일( <u>F</u> )  | 편집( <u>E</u> )  | 보기( <u>V</u> )                          | 터미널( <u>T</u> ) | 가기( <u>G</u> ) | 도움말( <u>H</u> ) |                   |   |
| # insmod<br>Using st<br>init mod<br># mknod<br># ./test<br>a. spin<br>b. back<br>select-> | step_dr<br>ep_drive<br>ule, STE<br>/dev/STE<br>_step<br>spin<br>a | iver.o<br>r.o<br>P major r<br>P c 247 ( | umber : 24      | 47             |                 |                   |   |
|   |   |   |                 |                |                 |                   | 2 |
|   |   |   |                 |                |                 |                   | * |

#### **Step Motor**

